

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**U.S. Patent Application of**

**OOSAWA et al.**

**Serial Number: 10/615,787**

**Filed: July 10, 2003**

**For: SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE**

**Attorney Docket No. HITA.0413**

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**Art Unit 2829**

**Examiner Jimmy Nguyen**

**Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231**

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

**TERMINAL DISCLAIMER**

Sir:

We, Stanley P. Fisher and Juan Carlos A. Marquez, represent that we are the attorneys of record for the above-identified application.

The invention has been assigned to Hitachi, Ltd., who is the owner by assignment of the entire right and title of the above-captioned patent application.

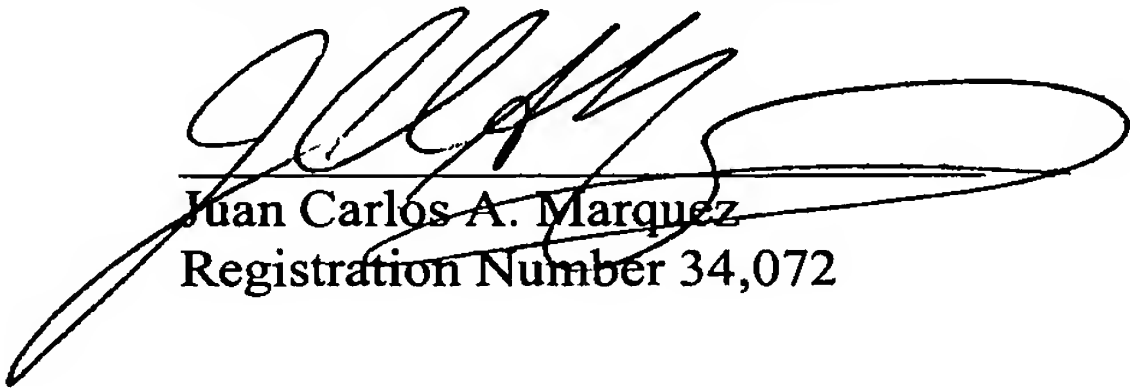
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Respectfully submitted,

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**December 22, 2004**

### **Substitute Abstract of the Disclosure**

An input/output pin for testing corresponding to a test circuit of the digital section is used in common as the input/output pin for normal operation of the analog section. The selection switches are respectively provided between the relevant analog pin and analog circuit and on a signal line up to the test circuit of the digital section from the relevant analog pin and the switches are provided at both end portions of the signal line between the test circuit of digital section and the input/output pin for common use in order to fix the voltage of the signal line to the predetermined voltage such as the ground voltage during the normal operation. Thereby, it is possible in a semiconductor integrated circuit having the analog and digital sections to eliminate any adverse effect, even if the input/output pin for testing corresponding to the test circuit of the digital section is used in common as the input/output pin for normal operation of the analog section, from the analog circuit due to the noise which is generated in the digital section and is then transferred to the analog circuit through the signal path up to the analog input/output pin connected to the test circuit from this test circuit of the digital section.